Notice of Allowability	Application No.	Applicant(s)	
	10/680,727	PELLIZZER ET AL.	
	Examiner	Art Unit	
	Roy K. Potter	2822	
The MAILING DATE of this communication apperation apperation all claims being allowable, PROSECUTION ON THE MERITS IS therewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. X This communication is responsive to the election filed 3/3/2	2005 in response to the restriction re	quirement.	
2. \boxtimes The allowed claim(s) is/are <u>1-14 and 20-36</u> .			
3. $igstyle igstyle igy igstyle igstyle igy igstyle igy igstyle igy igy igstyle igy igy igstyle igy igy igy igy igy igy igy igy$:		
4. Acknowledgment is made of a claim for foreign priority unexact and all b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the composition of the deposit attached Examiner's comment regarding REQUIREMENT for the composition of the deposit attached Examiner's comment regarding REQUIREMENT for the composition of the deposit attached Examiner's comment regarding REQUIREMENT for the composition of the deposit attached Examiner's comment regarding REQUIREMENT for the composition of the deposit attached Examiner's comment regarding REQUIREMENT for the composition of the deposit attached Examiner's comment regarding REQUIREMENT for the composition of the composition o	been received. been received in Application No cuments have been received in this r of this communication to file a reply of ENT of this application. itted. Note the attached EXAMINER' as reason(s) why the oath or declarate t be submitted. on's Patent Drawing Review (PTO-6) and Amendment / Comment or in the Of 84(c)) should be written on the drawing header according to 37 CFR 1.121(c) sit of BIOLOGICAL MATERIAL m	national stage applical complying with the recomplying with the recomplying with the recomplying with the recomplying state of the stat	quirements OTICE OF back) of
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal Pa 6. Interview Summary Paper No./Mail Date 8), 7. Examiner's Amendm 8. Examiner's Stateme 9. Other	(PTO-413), e nent/Comment	wance

REASONS FOR ALLOWANCE

Election/Restrictions

Applicant's election without traverse of claims 1-14 and 20-36 in the reply filed on 3/3/2005 is acknowledged.

The following is an examiner's statement of reasons for allowance:

The prior art does not teach or suggest the recited cell array comprising a plurality of cells, each having a bipolar transistor and a storage component where each active region is shared by at least two bipolar transistors. The prior art also does not teach or suggest the recited memory device with the base regions of the bipolar transistors formed in areas overlying the semiconductor body.

Jambotkar, U.S. Patent No. 4,981,807 discloses a process for fabricating complementary vertical transistor memory cell. As shown in figure 1, memory cell 100 comprises bipolar NPN transistors 10, 30 and PNP transistors 20 and 40. Device structure 200 shown in Figure 2D, shows vertical NPN transistor 10 and vertical PNP transistor 20.

Wong, U.S. Patent 5,276,638, discloses a bipolar memory cell with isolated PNP load.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Roy K. Potter whose telephone number is 571 272 1842. The examiner can normally be reached on M-F.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Roy K Potter Primary Examiner Art Unit 2822